## **TOSHIBA**

# MICROWAVE SEMICONDUCTOR TECHNICAL DATA

# MICROWAVE POWER GaAs FET TIM5964-4SL

#### **FEATURES**

- LOW INTERMODULATION DISTORTION
  - IM3=-45 dBc at Pout= 25.5dBm Single Carrier Level
- **HIGH POWER**

P1dB=36.5dBm at 5.9GHz to 6.4GHz

**■ HIGH GAIN** 

G1dB=9.0dB at 5.9GHz to 6.4GHz

- BROAD BAND INTERNALLY MATCHED FET
- HERMETICALLY SEALED PACKAGE

#### RF PERFORMANCE SPECIFICATIONS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Output Power at 1dB Gain	P1dB		dBm	35.5	36.5	
Compression Point						
Power Gain at 1dB Gain	G1dB	\/DC 40\/	dB	8.0	9.0	
Compression Point		VDS= 10V				
Drain Current	IDS1	f= 5.9 to 6.4GHz	Α		1.1	1.3
Gain Flatness	ΔG		dB	_	_	±0.6
Power Added Efficiency	ηadd		%		35	_
3 <sup>rd</sup> Order Intermodulation	IM3	Two-Tone Test	dBc	-42	-45	
Distortion		Po=25.5dBm				
Drain Current	IDS2	(Single Carrier Level)	Α	_	1.1	1.3
Channel Temperature Rise	ΔTch	(VDS X IDS + Pin – P1dB) X Rth(c-c)	°C			80

Recommended Gate Resistance(Rg): 150  $\Omega$  (Max.)

#### **ELECTRICAL CHARACTERISTICS (Ta= 25°C)**

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Transconductance	gm	VDS= 3V	mS		900	
		IDS= 1.5A				
Pinch-off Voltage	VGSoff	VDS= 3V	V	-1.0	-2.5	-4.0
		IDS= 15mA				
Saturated Drain Current	IDSS	VDS= 3V	Α	_	2.6	
		VGS= 0V				
Gate-Source Breakdown	VGSO	IGS= -50μA	V	-5		
Voltage						
Thermal Resistance	Rth(c-c)	Channel to Case	°C/W		4.5	6.5

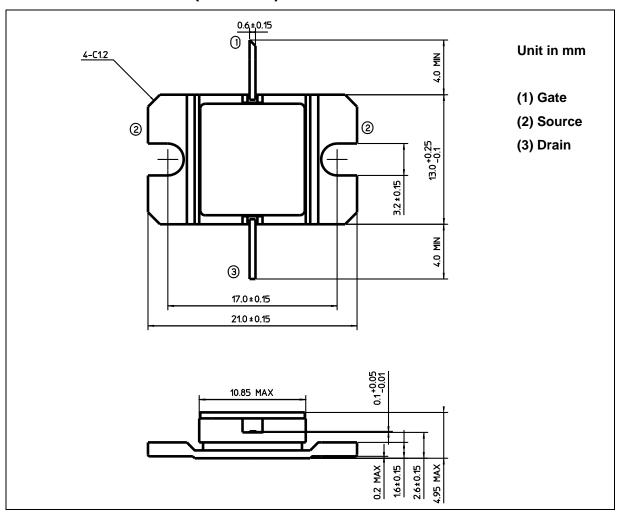
<sup>♦</sup> The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may results from its use, No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others.

The information contained herein is subject to change without prior notice. It is therefor advisable to contact TOSHIBA before proceeding with design of equipment incorporating this product.

### ABSOLUTE MAXIMUM RATINGS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	UNIT	RATING
Drain-Source Voltage	VDS	V	15
Gate-Source Voltage	VGS	V	-5
Drain Current	IDS	А	3.5
Total Power Dissipation (Tc= 25 °C)	PT	W	23.1
Channel Temperature	Tch	°C	175
Storage Temperature	Tstg	°C	-65 to +175

# **PACKAGE OUTLINE (2-11D1B)**

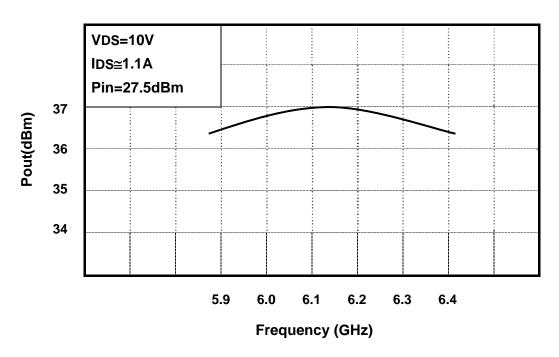


#### **HANDLING PRECAUTIONS FOR PACKAGE MODEL**

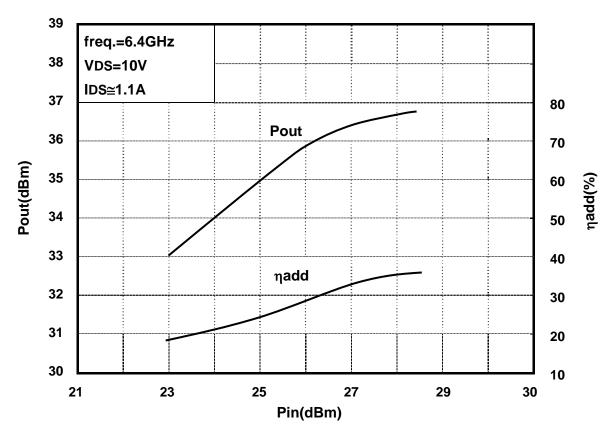
Soldering iron should be grounded and the operating time should not exceed 10 seconds at 260°C.

#### **RF PERFORMANCE**

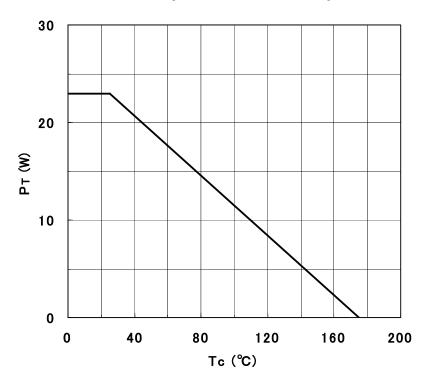
**Output Power vs. Frequency** 



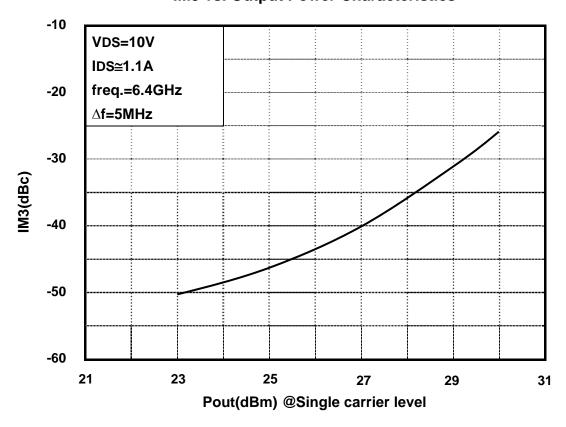
### **Output Power(Pout) vs. Input Power(Pin)**



### Power Dissipation vs. Case Temperature



**IM3 vs. Output Power Characteristics** 



## **TOSHIBA**

# MICROWAVE SEMICONDUCTOR TECHNICAL DATA

# MICROWAVE POWER GaAs FET TIM5964-4UL

#### **FEATURES**

- HIGH POWER
  P1dB=36.5dBm at 5.9GHz to 6.4GHz
- BROAD BAND INTERNALLY MATCHED FET
- HIGH GAIN
  G1dB=10.0dB at 5.9GHz to 6.4GHz
- **HERMETICALLY SEALED PACKAGE**

#### RF PERFORMANCE SPECIFICATIONS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Output Power at 1dB Gain	P1dB		dBm	35.5	36.5	_
Compression Point						
Power Gain at 1dB Gain	G1dB	VDS= 10V	dB	9.0	10.0	_
Compression Point		IDSset=0.9A				
Drain Current	IDS1	f = 5.9 to 6.4GHz	Α		1.1	1.3
Gain Flatness	ΔG		dB	_	_	±0.6
Power Added Efficiency	ηadd		%		37	
3rd Order Intermodulation	IM3	Two-Tone Test	dBc	-44	-47	_
Distortion		Po= 25.5dBm				
Drain Current	IDS2	(Single Carrier Level)	Α		1.1	1.3
Channel Temperature Rise	∆Tch	(VDS X IDS + Pin – P1dB) X Rth(c-c)	°C		_	80

Recommended gate resistance(Rg) : Rg= 150  $\Omega$ (MAX.)

## **ELECTRICAL CHARACTERISTICS** (Ta= 25°C)

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Transconductance	gm	VDS= 3V	mS	_	900	
		IDS= 1.5A				
Pinch-off Voltage	VGSoff	VDS= 3V	V	-1.0	-2.5	-4.0
		IDS= 15mA				
Saturated Drain Current	IDSS	VDS= 3V	Α	_	2.6	
		VGS= 0V				
Gate-Source Breakdown	VGSO	IGS= -50μA	V	-5	_	
Voltage		·				
Thermal Resistance	Rth(c-c)	Channel to Case	°C/W		4.5	6.0

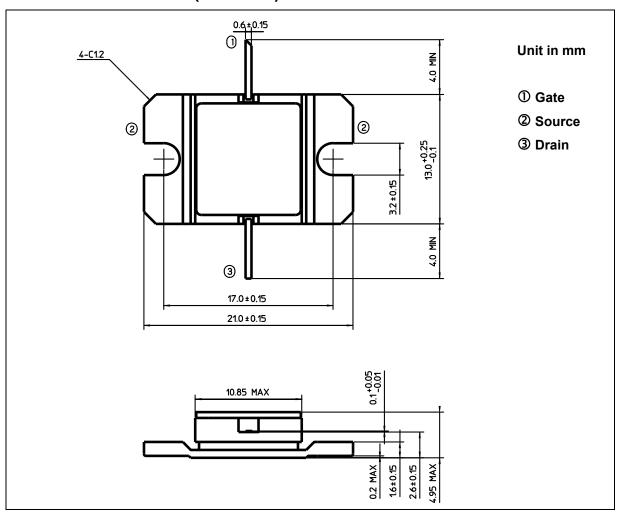
<sup>◆</sup> The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may results from its use, No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others.

The information contained herein is subject to change without prior notice. It is therefor advisable to contact TOSHIBA before proceeding with design of equipment incorporating this product.

## ABSOLUTE MAXIMUM RATINGS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	UNIT	RATING
Drain-Source Voltage	VDS	V	15
Gate-Source Voltage	VGS	V	-5
Drain Current	IDS	А	3.5
Total Power Dissipation (Tc= 25 °C)	PT	W	25
Channel Temperature	Tch	°C	175
Storage	Tstg	°C	-65 to +175

# PACKAGE OUTLINE (2-11D1B)

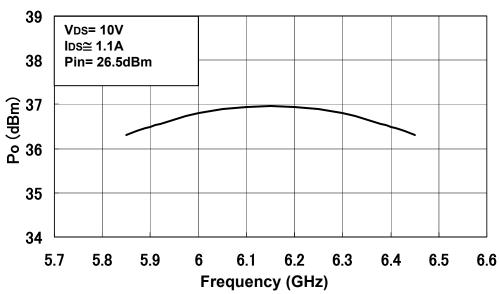


#### HANDLING PRECAUTIONS FOR PACKAGE MODEL

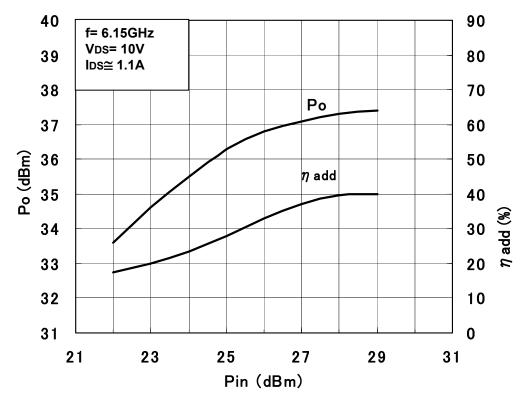
Soldering iron should be grounded and the operating time should not exceed 10 seconds at 260°C.

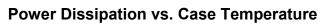
#### **RF PERFORMANCE**

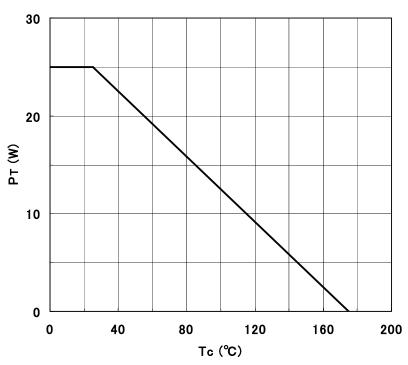




### **Output Power vs. Input Power**







IM3 vs. Output Power Characteristics

