



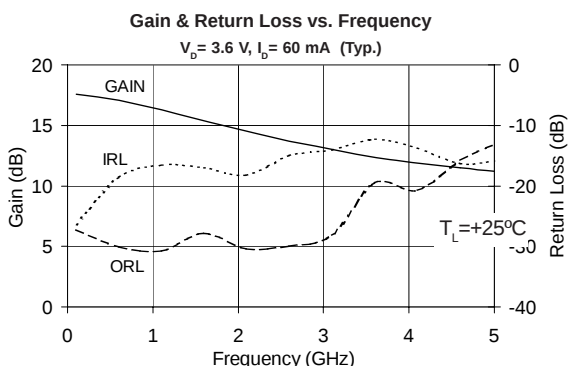
RFMD Green, RoHS Compliant, Pb-Free (Z Part Number)
Package: SOT-86

Product Description

The SGA-5386 is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one-micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.

Optimum Technology Matching® Applied

- ☐ GaAs HBT
- ☐ GaAs MESFET
- ☐ InGaP HBT
- ☐ SiGe BiCMOS
- ☐ Si BiCMOS
- ☒ SiGe HBT
- ☐ GaAs pHEMT
- ☐ Si CMOS
- ☐ Si BJT
- ☐ GaN HEMT
- ☐ RF MEMS



Features

- High Gain: 14.9 dB at 1950 MHz
- Cascadable 50Ω
- Operates from Single Supply
- Low Thermal Resistance Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Small Signal Gain	15.2	16.6	18.3	dB	850 MHz
		14.9		dB	1950 MHz
		14.0		dB	2400 MHz
Output Power at 1dB Compression		17.0		dBm	850 MHz
		14.7		dBm	1950 MHz
Output Third Intercept Point		32.0		dBm	850 MHz
		29.0		dBm	1950 MHz
Bandwidth Determined by Return Loss		5000		MHz	>10 dB
Input Return Loss		18.5		dB	1950 MHz
Output Return Loss		30.0		dB	1950 MHz
Noise Figure		4.0		dB	1950 MHz
Device Operating Voltage	3.1	3.6	4.1	V	
Device Operating Current	54	60	66	mA	
Thermal Resistance (Junction - Lead)		97		$^\circ\text{C}/\text{W}$	

Test Conditions: $V_S = 8\text{ V}$, $I_D = 60\text{ mA}$ Typ., OIP_3 Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, $R_{\text{BIAS}} = 75\Omega$, $T_L = 25^\circ\text{C}$, $Z_S = Z_L = 50\Omega$

Absolute Maximum Ratings

Parameter	Rating	Unit
Max Device Current (I_D)	120	mA
Max Device Voltage (V_D)	5	V
Max RF Input Power	+16	dBm
Max Junction Temp (T_J)	+150	°C
Operating Temp Range (T_L)	-40 to +85	°C
Max Storage Temp	+150	°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression:

$$I_D V_D < (T_J - T_L) / R_{TH, J-I}$$



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

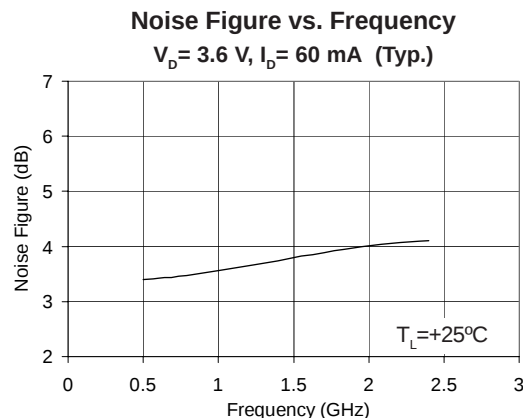
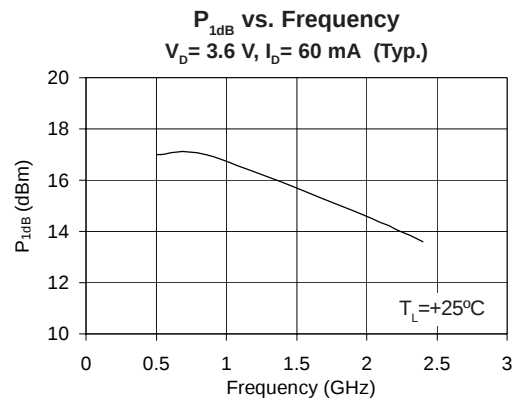
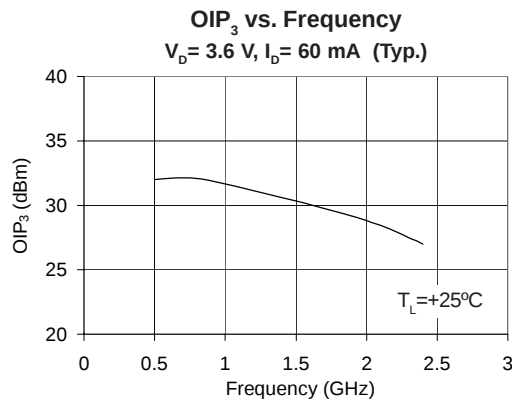
RoHS status based on EU Directive 2002/95/EC (at time of this document revision).

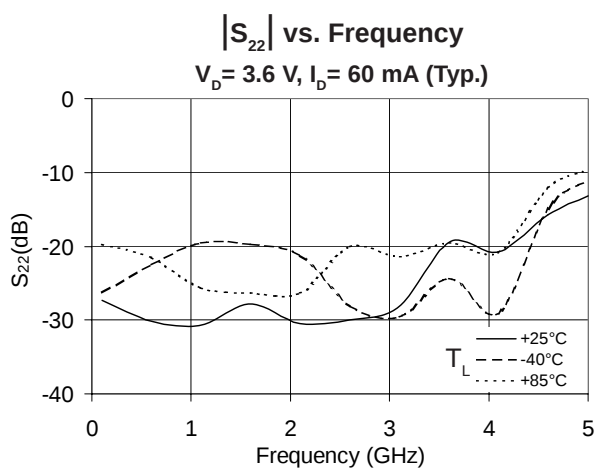
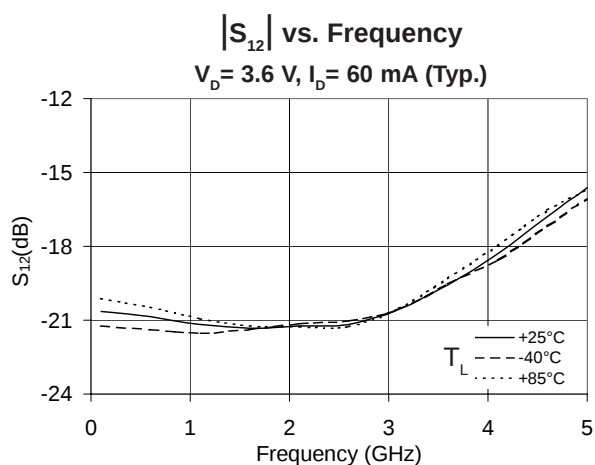
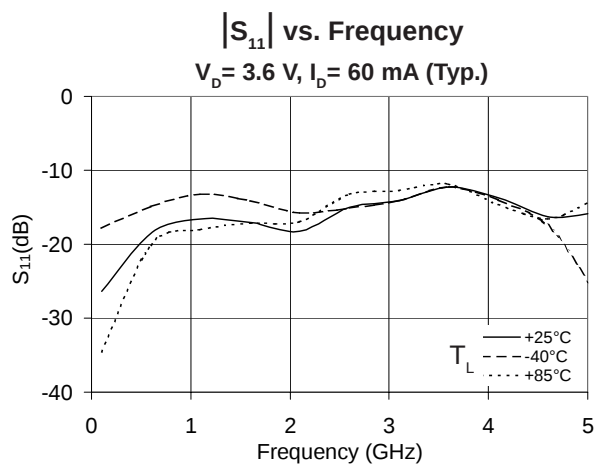
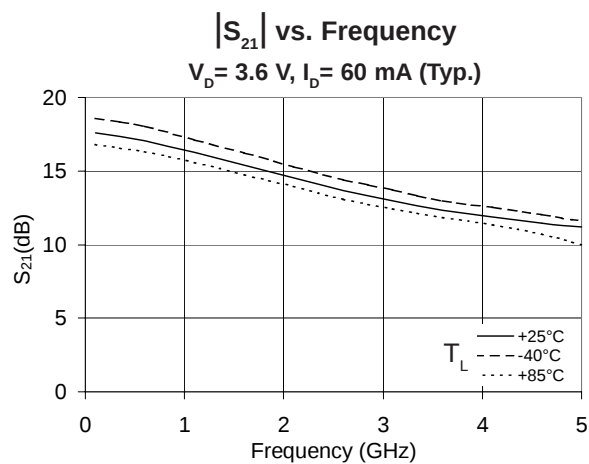
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Typical Performance at Key Operating Frequencies

Parameter	Unit	100 MHz	500 MHz	850 MHz	1950 MHz	2400 MHz	3500 MHz
Small Signal Gain	dB	17.6	17.2	16.6	14.7	14.0	12.5
Output Third Order Intercept Point	dBm		32.0	32.0	29.0	27.0	
Output Power at 1dB Compression	dBm		17.0	17.0	14.7	13.6	
Input Return Loss	dB	26.4	19.6	16.9	18.5	15.8	11.6
Output Return Loss	dB	27.3	29.5	31.0	30.0	33.4	19.1
Reverse Isolation	dB	20.6	20.8	21.1	21.2	21.2	19.8
Noise Figure	dB		3.4	3.5	4.0	4.1	

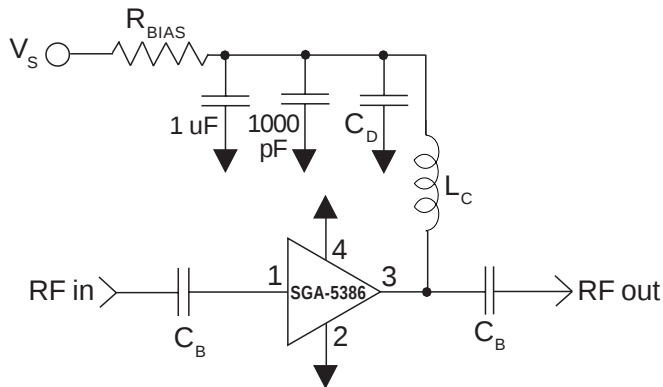
Test Conditions: $V_S = 8V$, $I_D = 60mA$ Typ., OIP_3 Tone Spacing = 1MHz, P_{OUT} per tone = 0dBm, $R_{BIAS} = 75\Omega$, $T_L = 25^\circ C$, $Z_S = Z_L = 50\Omega$





Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC-blocking capacitor is necessary for proper operation.

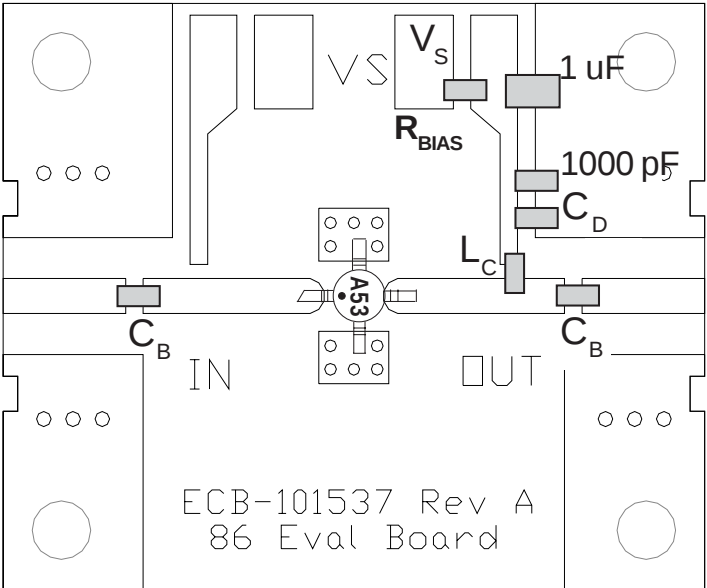
Application Schematic



Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C _B	220 pF	100 pF	68 pF	56 pF	39 pF
C _D	100 pF	68 pF	22 pF	22 pF	15 pF
L _C	68 nH	33 nH	22 nH	18 nH	15 nH

Recommended Bias Resistor Values for I _D =60mA				
$R_{BIAS} = (V_S - V_D) / I_D$				
Supply Voltage(V _S)	6 V	8 V	10 V	12 V
R _{BIAS}	39 Ω	75 Ω	110 Ω	150 Ω
Note: R _{BIAS} provides DC bias stability over temperature.				

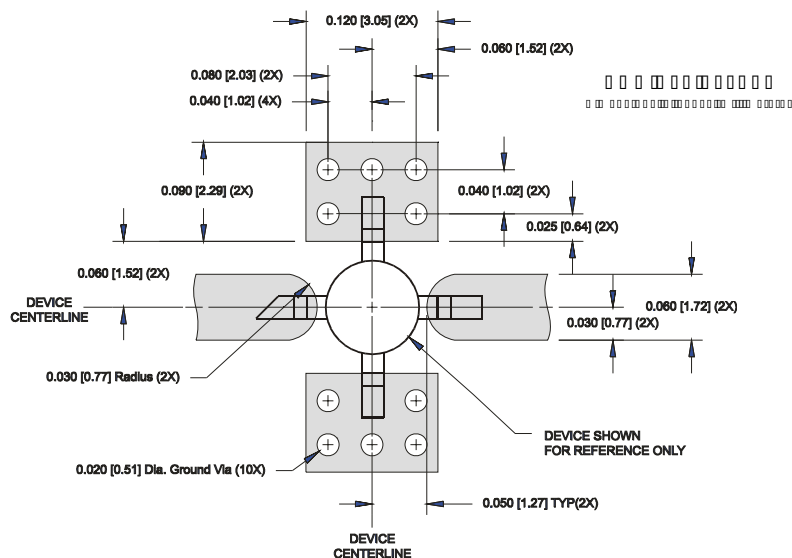
Evaluation Board Layout



Mounting Instructions

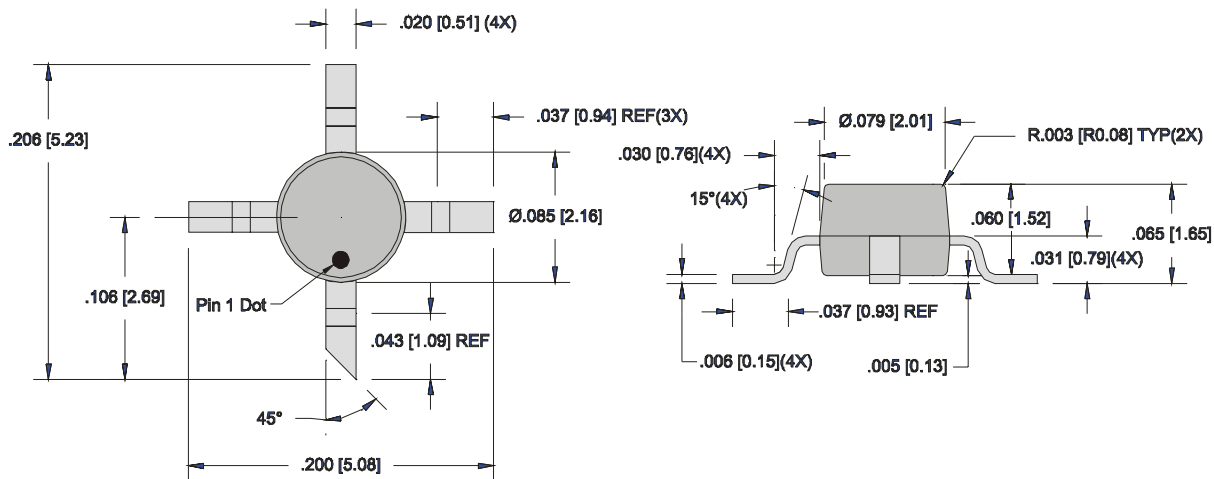
1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Suggested Pad Layout

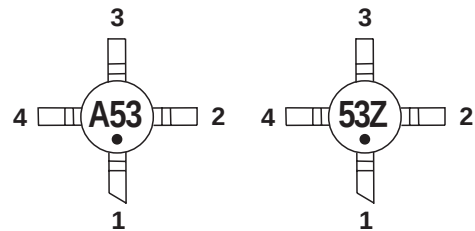


Package Drawing

Dimensions in inches (millimeters)
Refer to drawing posted at www.rfmd.com for tolerances.



Part Identification



Ordering Information

Part Number	Reel Size	Devices/Reel
SGA-5386	13"	3000
SGA-5386Z	13"	3000